BA-4000 Bit Analyzer

800G BIT ERROR RATE (BER) TESTER

Electrical BER tester supporting NRZ and PAM4 coding, with advanced FEC tools and with testing capabilities up to 800G.



KEY FEATURES

Supports NRZ and PAM4

Supports PRBS 7/9/11/13/15/23/31/13Q/31Q, SSPRQ

Advanced FEC tools

Supports RS-FEC Scrambled Idle Pattern

Channel simulator

Burst/random error injection

O-SMPM connection

Channel histogram

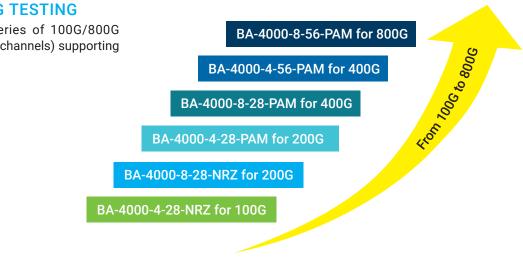
Channel mapping

Automation: API support



BA-4000 READY FOR 800G TESTING

The BA-4000 is a world-class series of 100G/800G electrical BER testers (either 4 or 8 channels) supporting PAM4 or NRZ coding.



POWERFUL AND SIMPLIFIED USER INTERFACE

The BA-4000 graphical user interface (GUI) provides simplified and real-time test results per channel. It requires an external Windows-based PC with Ethernet capability to run the GUI and API.

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EXFO	v 5.3.2.1 Setup Help		Ó	6			
	MultiRate 1.5Vpp	Symbol Rate (26.5625 GBd PAM4			BER Configuration	Relock
172.16.81.65	FEC 56G	Clock (A-B)	Rate/8		TX/RX Configuration	Run 💽 💽 🌧	Force Relock
		Ī		İ			
	Channel 1		Channel 2		Channel 3	Channel 4	
Pre BER	8.737e-09 🔍 🏓	Pre BER	2.372e-09 💌 🍰	Pre BER	3.636e-05 🔍 🏓	Pre BER 3.834e	e-09 💌 🍰
Pre Errors	4,696 PN31	Pre Errors	1,279 PN31	Pre Errors	19,674,924 PN31	Pre Errors 2,	082 PN31
Corrected	4,696 PN31	Corrected	1,279 PN31	Corrected	19,674,924 PN31	Corrected 2,	082 PN31
Post BER	0.000e+00 Sync	Post BER	0.000e+00 Sync	Post BER	0.000e+00 Sync	Post BER 0.000e	+00 Sync
Margin	80% (max: 3) KP4	Margin	80% (max: 3) KP4	Margin	40% (max: 9) KP4	Margin 80% (max	(: 3) KP4
# Bits	537,460,265,600	# Bits	539,295,804,160	# Bits	541,134,126,592	# Bits 542,9	968,437,504
Time	10 s	Time	10 s	Time	10 s	Time 10 s	
	Channel 5		Channel 6		Channel 7	Channel 8	
Pre BER	3.671e-11 🔍 🏓	Pre BER	3.489e-06) 💌 🏓	Pre BER	2.545e-05) 💿 🍰	Pre BER 1.976e	2-06 💽 🏄
Pre Errors	20 PN31	Pre Errors	1,907,057 PN31	Pre Errors	13,961,536 PN31	Pre Errors 1,052,	889 PN31
Corrected	20 PN31	Corrected	1,907,057 PN31	Corrected	13,961,536 PN31	Corrected 1,052,	889 PN31
Post BER	0.000e+00 Sync	Post BER	0.000e+00 Sync	Post BER	0.000e+00 Sync	Post BER 0.000e	+00 Sync
Margin	80% (max: 3) KP4	Margin	80% (max: 3) KP4	Margin	60% (max: 6) KP4	Margin 80% (max	(: 3) KP4
# Bits	544,804,921,728	# Bits	546,644,141,824	# Bits	548,487,245,696	# Bits 532,7	769,596,416
Time	10 s	Time	10 s	Time	10 s	Time 10 s	
			PPG	BER	Monitor FEC	CH Simulation	MA
Error Injection	BER Results FEC Results						
UpdateTaps(0,0	0,800,0,0,0);					Done	



FEC SIMULATION

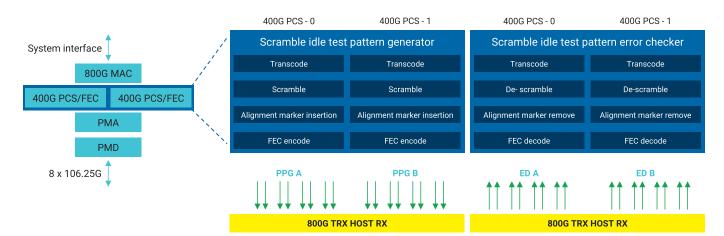
The BER tester includes FEC simulation capabilities. This provides powerful burst error analysis.

Main features include:

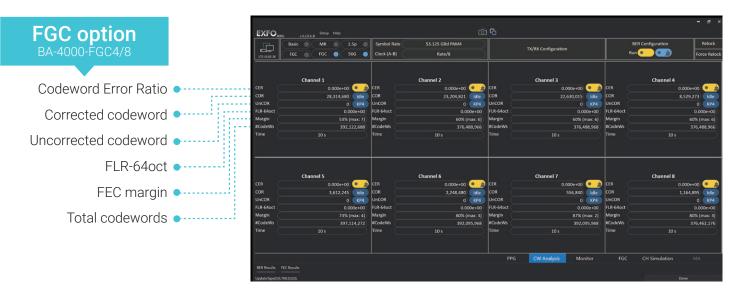
- PRBS error check and correction
- Pre-FEC and Post-FEC BER
- KP4/KR4 and low latency FEC protocols
- FEC lane striping function
- · FEC symbol error distribution plot: codewords vs symbol errors
- FEC margin auto-calculation

FEC encoded scrambled idle

With the FEC Generator and Checker (FGC) option, the BA-4000 addresses RS-FEC scrambled idle pattern for testing 53 GBd host side interfaces as part of the development of new-generation 800G optics, including optical transceivers, DAC, etc.



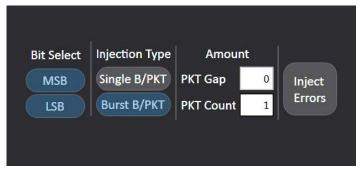
⁸⁰⁰G function flow structure



FEC encoded scramble idle metrics in the GUI



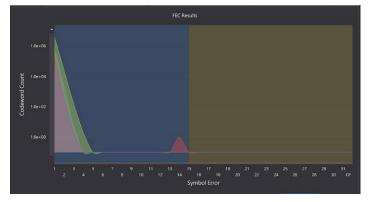
WITH PAM4 CODING, A SIMPLE BER TEST IS NOT ENOUGH



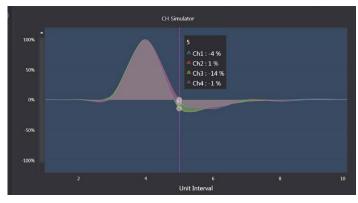
Pre BER	5.003e-08	• ;	
Pre Errors	26,581	PN31	
Corrected	26,581	PN31	
Post BER	0.000e+00	Sync	
Margin	87% (max: 2)	KP4	
# Bits	531,315,833,984		
Time	10 s		

Burst and random error injection





FEC symbol error distribution plot



Channel response simulation



Channel histogram



7-tap mode



All specifications are typical, at 23 °C ± 2 °C unless otherwise specified.

SPECIFICATIONS				
BA-4000	x-28-NRZ	x-28-PAM	x-56-PAM	x-56-PAM-FGC
Number of channels	4 (x = 4) 8 (x = 8)	4 (x = 4) 8 (x = 8)	4 (x = 4) 8 (x = 8)	4 (x = 4) 8 (x = 8)
Modulation	NRZ	NRZ/PAM4	NRZ/PAM4	NRZ/PAM4
Data rate per lane ª (GBd)	9.95328, 10, 10.3125, 10.709, 11.3176, 12.5, 14.025, 24.33024, 25, 25.78125, 26.5625, 27.95, 28.05, 28.125	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 49.765, 53.125, 57.8	24.8832, 25.0, 25.06752, 25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 49.765, 49.7664, 50.0, 50.13504, 51.5625, 53.125, 55.9, 57.8
Data rate per lane (GBd) under FEC mode	n/a	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9	25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9	24.8832, 25.0, 25.06752, 25.78125, 26.5625, 27.95, 28.05, 28.125, 28.9, 49.765, 49.7664, 50.0, 50.13504, 51.5625, 53.125, 55.9, 57.8
Data rate per lane (GBd) under FGC mode	n/a	n/a	n/a	25.78125, 26.5625 (support NRZ and PAM4), 51.5625, 53.125 (support PAM4)
Data rate adjustment (ppm)	0 to ±300	0 to ±300	0 to ±300	0 to ±300
PAM4 coding	n/a	Linear code / Gray code	Linear code / Gray code	Linear code / Gray code
Pattern supported by PPG and ED	PRBS 7/9/15/23/31 and user-defined pattern	PRBS 7/9/11/13/15/23/31 PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ, and user-defined pattern	PRBS 7/9/11/13/15/23/31 PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ, and user-defined pattern	PRBS 7/9/11/13/15/23/31 PRBS 7Q/9Q/11Q/13Q/ 15Q/23Q/31Q Only PPG supports PRBS16Q, SSPRQ, and user-defined pattern Scrambled Idle at FGC mode (with option FGCx)
Pattern supported by PPG and ED under FEC mode	n/a	PRBS 7/9/11/15/23/31 PRBS 7Q/9Q/11Q/15Q/ 23Q/31Q	PRBS 7/9/11/15/23/31 PRBS 7Q/9Q/11Q/15Q/ 23Q/31Q	PRBS 7/9/11/15/23/31 PRBS 7Q/9Q/11Q/15Q/ 23Q/31Q
Maximum amplitude (mV _{ppd})	800 ^{b, c}	800 c, e, j	800 ^{f, j}	800 ^{f, j}
Rise time/fall time (20% to 80%) (ps)	15/15°	11/11°	9.5/9.5° (53.125G) 10/10° (25.78125G)	10/10° (53.125G) 10/10° (25.78125G)
PAM4 eye width (zero hit) (ps)	n/a	23 ^d	5.5 ^f (53.125G) 23 ^d (26.5625G)	5.5 ^f (53.125G) 23 ^d (26.5625G)
Jitter RMS (fs)	750°	450°	400° (53.125G) 450° (25.78125G)	500 ° (53.125G) 450 ° (25.78125G)
Sensitivity (m V_{ppd})	100 (NRZ 25.78125G)	200 (PAM4 26.5625G)	200 ^h (PAM4 53.125G)	250 ^{h, i} (PAM4 53.125G)
CTLE (dB)	0 to 7	0 to 8	n/a	n/a
ED damage level (mV $_{\rm ppd}$)	1200	1200	1200	1200
Clock output amplitude $(mV_{_{ppd}})$	300	400	400	400
Clock ratio	/8, /16 (Clock frequency / Symbol rate)	/2, /4, /8, /16, /32, /64 (Clock frequency / Symbol rate)	/2, /4, /8, /16, /32, /64 (Clock frequency / Symbol rate)	/2, /4, /8, /16, /32, /64 (Clock frequency / Symbol rate)
Connector type	O-SMPM connector (up to 67 GHz bandwidth)	O-SMPM connector (up to 67 GHz bandwidth)	O-SMPM connector (up to 67 GHz bandwidth)	O-SMPM connector (up to 67 GHz bandwidth)

a. Fixed rate.

b. Amplitude step is 200 $\mathrm{mV}_{_{\mathrm{ppd}}}$

c. NRZ 25.78125 GBd signal measured by 50 GHz bandwidth scope with 40 GHz 2.92 mm, 15 cm RF cable.

 PAM 26.5625 GBd signal measured by 50 GHz bandwidth scope with 40 GHz 2.92 mm, 15 cm RF cable. e. NRZ 53.125 GBd signal measured by 50 GHz bandwidth scope with 50 GHz 2.4 mm, 15 cm RF cable. Post-cursor is –2%.

f. PAM4 53.125 GBd signal measured by 50 GHz bandwidth scope with 50 GHz 2.4 mm, 15 cm RF cable. Post-cursor is -2%.

g. Measured by direct loopback from PPG to ED with 40 GHz O-SMPM, 20 cm RF cable.

h. BER ≤ 10⁻¹⁰

i. If greater sensitivity is required, please contact EXFO for the high-performance model.

j. Support overdrive 900 mV $_{\rm ppd}$



EXFO

GENERAL SPECIFICATIONS	
Size (H x W x D)	103 mm x 442 mm x 300 mm (4.1 in x 17.4 in x 11.8 in)
Weight	≤ 10 kg (22 lb)
Temperature Operating Storage	5 °C to 40 °C (41 °F to 104 °F) −20 °C to 70 °C (−4 °F to 158 °F)
Relative humidity	20% to 80%
Power ^a	100/120 Vac (50/60/400 Hz) 220/240 Vac (50/60 Hz) 60 W typical/80 W max.

a. Operate with supply voltage fluctuations up to ±10 % of the nominal voltage.

AVAILABLE OPTIONS

BA-4000	FEC4	FEC8	FGC4	FGC8
4-28-NRZ				
8-28-NRZ				
4-28-PAM	~			
8-28-PAM		\checkmark		
4-56-PAM	~		~	
8-56-PAM		~		~

BA-4000-)	X-XX
Models 4-28-NRZ = 4x28 GBd NRZ BERT with 0-SMPM connector 8-28-NRZ = 8x28 GBd NRZ BERT with 0-SMPM connector 4-28-PAM = 4x28 GBd NRZ/PAM4 BERT with 0-SMPM connector 8-28-PAM = 8x28 GBd NRZ/PAM4 BERT with 0-SMPM connector 4-56-PAM = 4x56 GBd NRZ/PAM4 BERT with 0-SMPM connector 8-56-PAM = 8x56 GBd NRZ/PAM4 BERT with 0-SMPM connector	Options FEC4 = 26G PAM4 FEC simulator software 4CH a FEC8 = 26G PAM4 FEC simulator software 8CH b FGC4 = FEC pattern generator and checker 4CH c FGC8 = FEC pattern generator and checker 8CH d

a. Available for BA-4000-4-28-PAM and BA-4000-4-56-PAM.

b. Available for BA-4000-8-28-PAM and BA-4000-8-56-PAM.

c. Available for BA-4000-4-56-PAM. Must be ordered with FEC4 software option.

d. Available for BA-4000-8-56-PAM. Must be ordered with FEC8 software option.

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